

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3343597	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:28
L2	27	1 and one near3 transistor near3 split adj gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:29
L3	1651	1 and split adj gate near3 cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:40
L4	1664	2 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:30
L5	130	3 and split adj3 source	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:30
L6	5	3 and split adj3 source with (odd or even)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:34
L7	86	1 and cell near4 (two or "2") near3 transistor with split adj gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:36
L8	10	7 and (even or odd) with source	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:37
L9	130	3 and split adj3 source	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:41
L10	76	9 and (odd or even)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:41
L11	1	10 and second adj3 program\$5 adj (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 11:42

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	0	((memory or storage) and spilt adj gate and even and odd and split adj source and segment).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 14:50
L3	0	((memory or storage) and one adj transistor adj3 spilt adj gate and even and odd and split adj source and segment).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/01 14:50